



I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage as First Class Mail under 37 CFR 1.8 on the date indicated below and is addressed to: "Commissioner for Patents, Alexandria, VA 22313-1450."

Signed Stuart T. Auvinen
Stuart T. Auvinen, Reg. No. 36,435

Date Signed and Mailed 12-29-03

PATENT
ML-18

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
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| In re application of |) | |
| |) | |
| Chou et al. |) | Examiner: |
| |) | |
| Serial No. 10/605,140 |) | Group Art Unit: 2811 |
| |) | |
| Filed: 9/10/2003 |) | |
| |) | |
| For: Flash Drive/Reader with Serial-Port Controller |) | |
| and Flash-Memory Controller Mastering a Second |) | |
| RAM-Buffer Bus Parallel to a CPU Bus |) | |
| |) | |

SECOND INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks
Washington, DC 20231

Sir:

Attached is a completed Substitute Form PTO-1449 and copies of the pertinent parts of the references cited thereon.

- ☒ The commissioner is hereby authorized to charge payment of any patent application processing fees under 37 C.F.R. § 1.17 associated with this communication or credit any overpayment to deposit account No. 01-2950.

Respectfully Submitted,

Stuart T. Auvinen
Agent for Applicant
Reg. No. 36,435

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| Form PTO-1449 | | | | | | Docket Number (Optional) ML-18 | | Application Number 10/605,140 | | | | | |
| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) | | | | | | Applicant Chou et al. | | | | | | | |
| | | | | | | Filing Date 9/10/2003 | | Group Art Unit 2811 | | | | | |
| U.S. PATENT DOCUMENTS | | | | | | | | | | | | | |
| EXAMINER INITIAL | DOCUMENT NUMBER | | | | | | | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | | | | | | | |
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| OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | | | | | | |
| | | Certified USB, "Introduction to USB On-The-Go", 9/03, pp 1 | | | | | | | | | | | |
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| | | Ours Technology Inc., "OTi-2168" Data Sheet, 9/03, pp 1-8 | | | | | | | | | | | |
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| | | Genesys Logic, Inc., "GL814-" specification, 12/10/02, pp 1-21 | | | | | | | | | | | |
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| EXAMINER | | | | | | | | | DATE CONSIDERED | | | | |
| EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant. | | | | | | | | | | | | | |

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)

ML-18

Application Number

10/605,140

Applicant

Chou et al.

Filing Date

9/10/2003

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| Group Art Unit |
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2811

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

| | DOCUMENT NUMBER | | | | | | | DATE | NAME | CLASS | SUBCLASS | TRANSLATION | |
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| | | Certified USB, "Introduction to USB On-The-Go", 9/03, pp 1 |
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